

ABSTRACT OF THE DISCLOSURE

This invention defines a highly programmable MAC architecture for handling protocols that require precision timing and demand very short response times. The Media Access Controller consists of micro-coded programmable co-processors and general purpose CPUs. CPUs perform processing intensive functions while co-processors perform PHY specific media access control functions. The uniqueness of the architecture is in the real-time programmability of the co-processors; they can be reprogrammed by the CPUs based on the calculations performed in the CPU domain.

Any embodiment of this invention is suitable for ASIC, FPGA, discrete or combinations of these implementation schemes. The invention applies to any communications technology.

REFERENCES INCORPORATED BY REFERENCE HEREIN

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[3] ETSI TS 101 867 V1.1.1 (2000-11); Technical Specification --
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